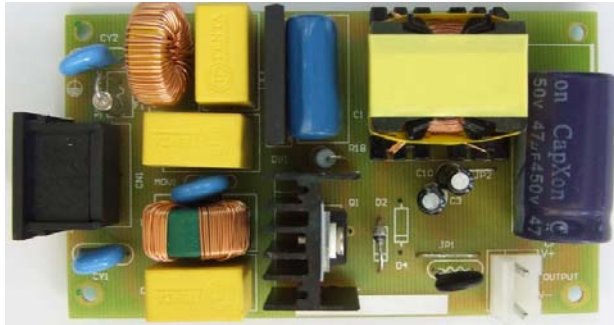


Subject
OB6561P Demo Board Manual

Board Model: OP400V0.2A6561P.A2
 Doc. No.: OB_DOC_DBM_61PA2



Key features:

- Transition Mode Operation
- System Open Loop Protection
- Audio Noise Free
- Quadrant Multiplier With THD Optimizer
- Dynamic and Static Output Over-Voltage Protection
- Internal Start-up Timer for Stand-alone Applications

Revision History

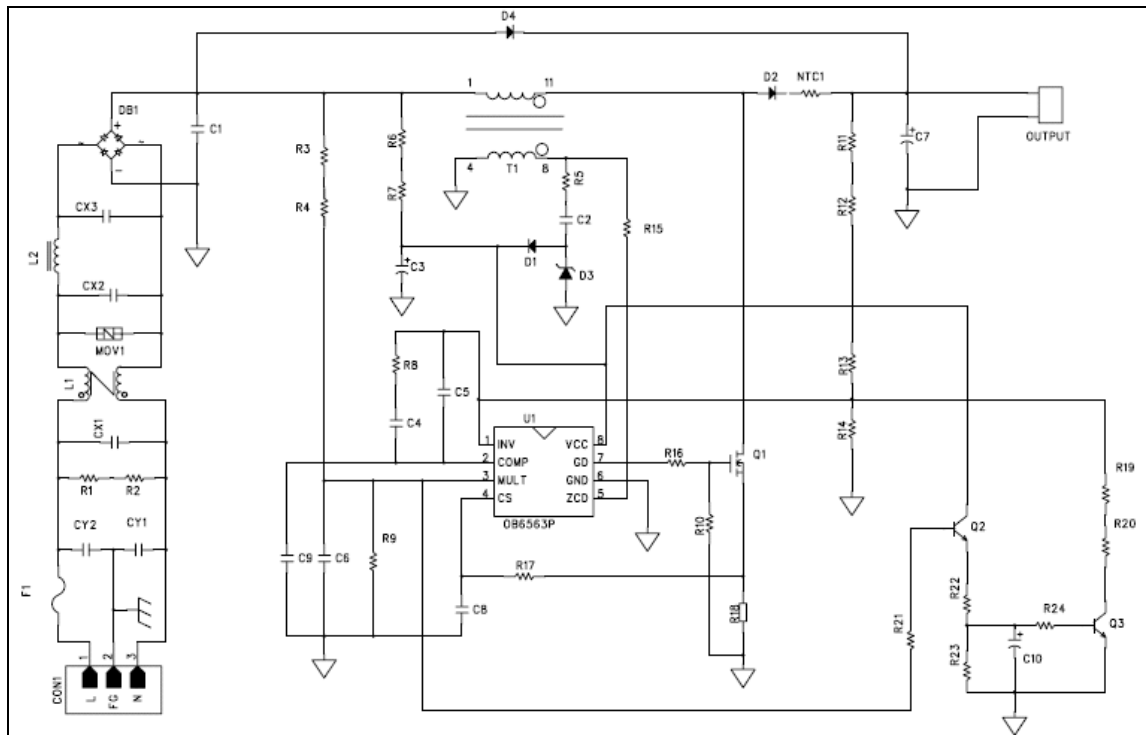
Revise Date	Version	Reason/Issue
2007-09-28	A2	BOM Check Update Format & Schematic

1 System Specifications and Test Results

Items	Specifications & Test Conditions	Test Results
1. Input Characteristic		
Input Voltage Range	90Vac to 264Vac @ Full load	PASS
AC Input Frequency	47Hz to 63Hz @ Full load	PASS
Maximum input current	1.0 A Max. @ 90Vac, Full load	0.989A
Input power factor	>0.9 @ Full load, 90~264Vac	Min. 0.942
Input current THD	<20% @ Full load, 90~264Vac	Max. 9.2%
Efficiency	>90% @ Full load, 90~264Vac	92.3%
2. Output Characteristic		
Output Voltage	400±10V @ 0~100% load, 90~264Vac	PASS
Line regulation	<1% @ 0~100% load, 90~264Vac	PASS
Load regulation	<5% @ 0~100% load, 90~264Vac	PASS
Output Current Range	0 to 0.2A @ 90~264Vac	PASS
Ripple voltage	<50Vp-p Max. @ 0~100% load, 90~264Vac	Max. 19.5V
Output Voltage Overshoot	Peak Output Voltage<450V @ 0~100% load, 90~264Vac	Max. 441V
Note: All data was measured at PCB end if not otherwise noted.		

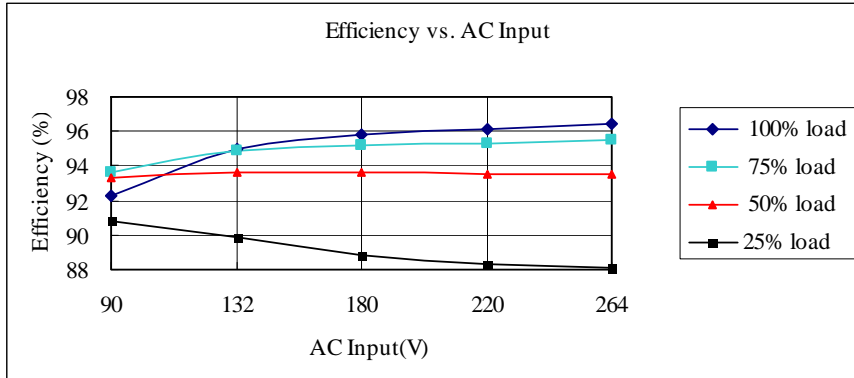
2 System Information

2.1 Schematic

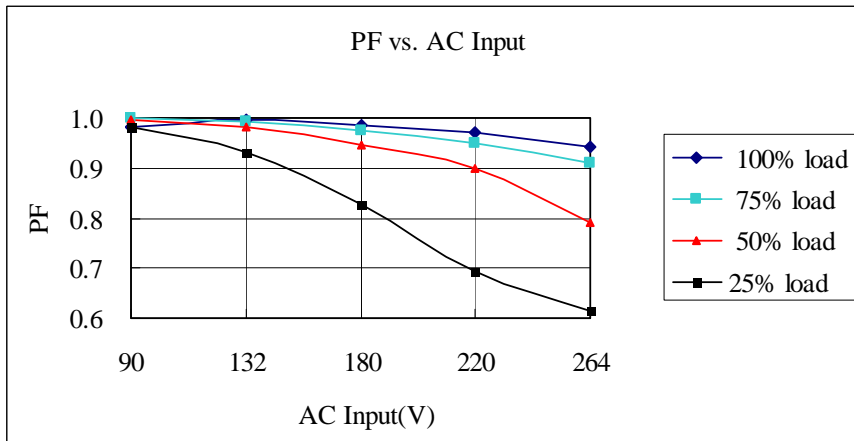


3 Performance Evaluation

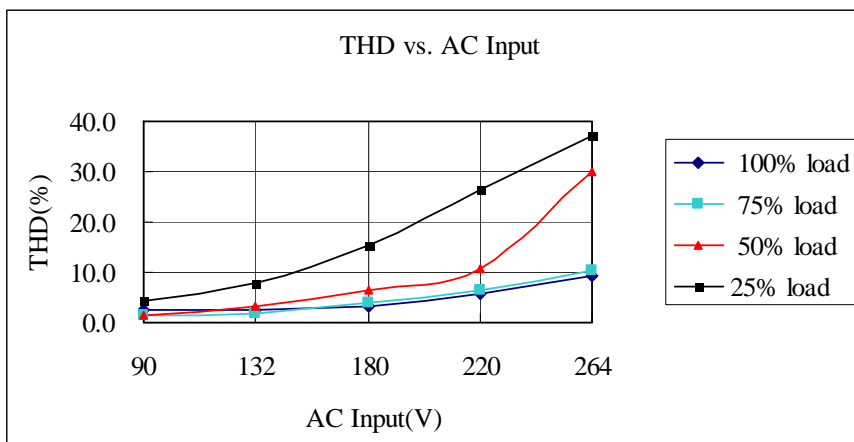
3.1 Efficiency



3.2 Power Factor



3.3 Total Harmonic Distortion



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